

EV318283495

Inventor: **F r nando Gonzalez**
Title: **Methods of F rming Semiconductor Constructions**
Assignee: **Micron Techn logy, Inc.**
PRIORITY Serial No.: **09/592,604**
PRIORITY Filing Date: **June 12, 2000**

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§1.56, 1.97 AND 1.98

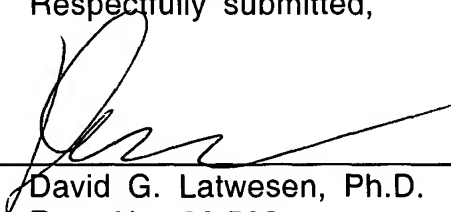
In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation of co-pending application Serial No. 09/592,604 filed June 12, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: September 9, 2003 Attorney:



David G. Latwesen, Ph.D.
Reg. No. 38,533
Wells, St. John P.S.

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2393		PRIORITY SERIAL NO. 09/592,604	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Fernando Gonzalez			
				PRIORITY FILING DATE June 12, 2000		PRIORITY GROUP 2818	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,298,449	03/94	Kikuchi			
	AB	5,374,564	12/94	Bruel			
	AC	5,855,693	01/99	Murari et al.			
	AD	5,877,070	03/99	Goesele et al.			
	AE	5,882,987	03/99	Srikrishnan			
	AF	5,894,152	04/99	Jaso et al.			
	AG	5,953,622	09/99	Lee et al.			
	AH	6,004,406	12/99	Kobayashi et al.			
	AI	6,309,945 B1	10/01	Sato et al.			
	AJ	6,429,070 B1	08/02	Gonzalez et al.			
	AK	6,384,439 B1	05/02	Walker			
		6,423,992 B2	07/02	Fukuda, et al.			
	AL	5,998,847	12/99	Assaderaghi, et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Gösele, U. et al., "Semiconductor Wafer Bonding: Science, Technology, and Applications", Electrochemical Society Proceedings				
			Vol. 97-36, (©1998), pp. 400-425, 436-445.				
	AS						
EXAMINER				DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							

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Form PTO-1449

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MI22-2393PRIORITY SERIAL NO.
09/592,604LIST OF ART CITED BY APPLICANT
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U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,251,754	6/01	Kobayashi et al.			
	AB	6,245,161	6/01	Henley et al.			
	AC	5,374,581	12/94	Ichikawa et al.			
	AD	4,891,329	1/90	Reisman et al.			
	AE	6,150,031	11/00	Yonehara			
	AF	6,083,324	7/00	Henley et al.			
	AG						
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AN							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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